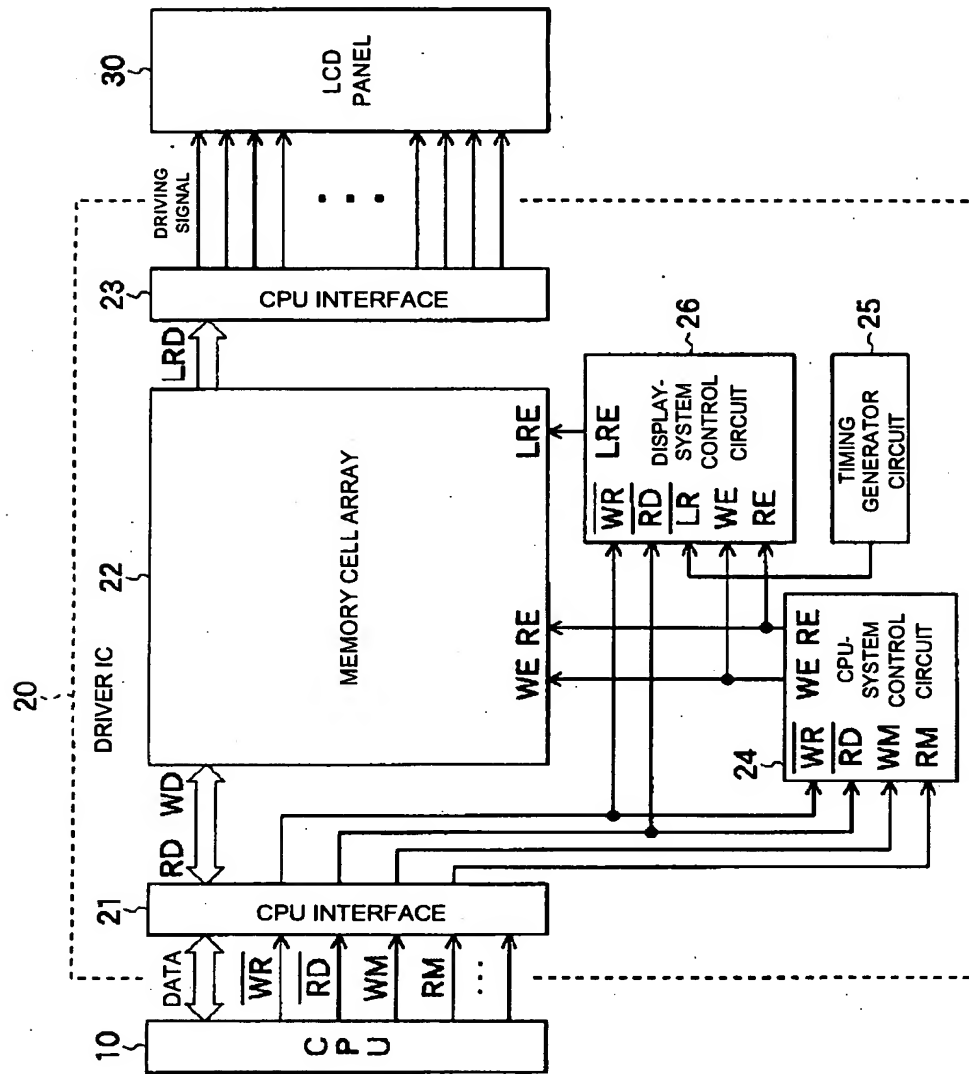


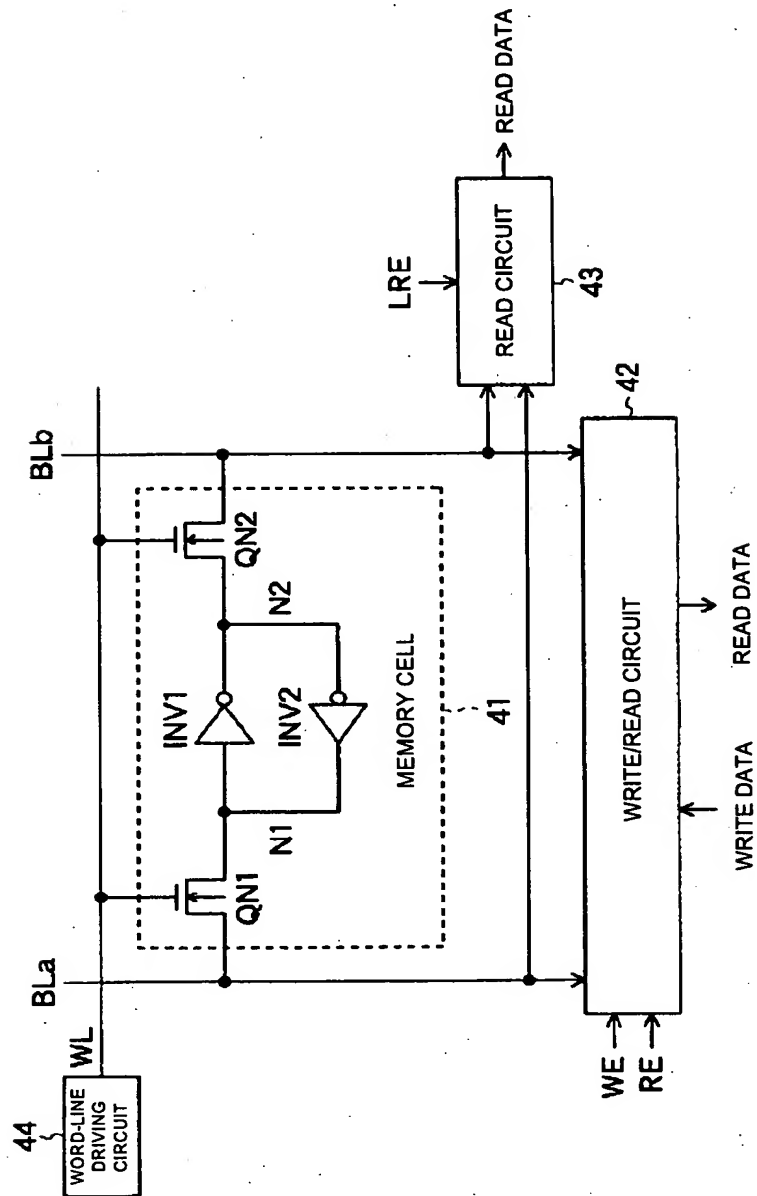
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FIG.1



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FIG.2



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FIG.3

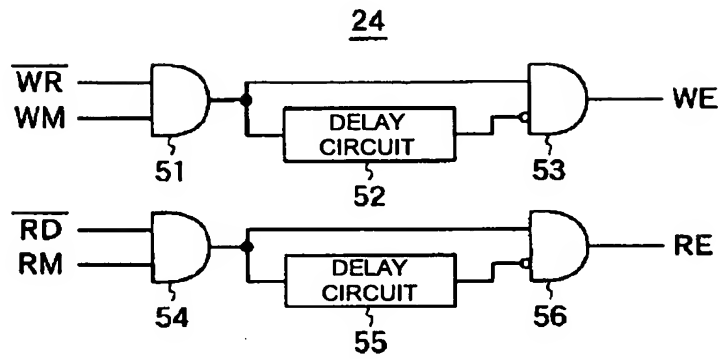
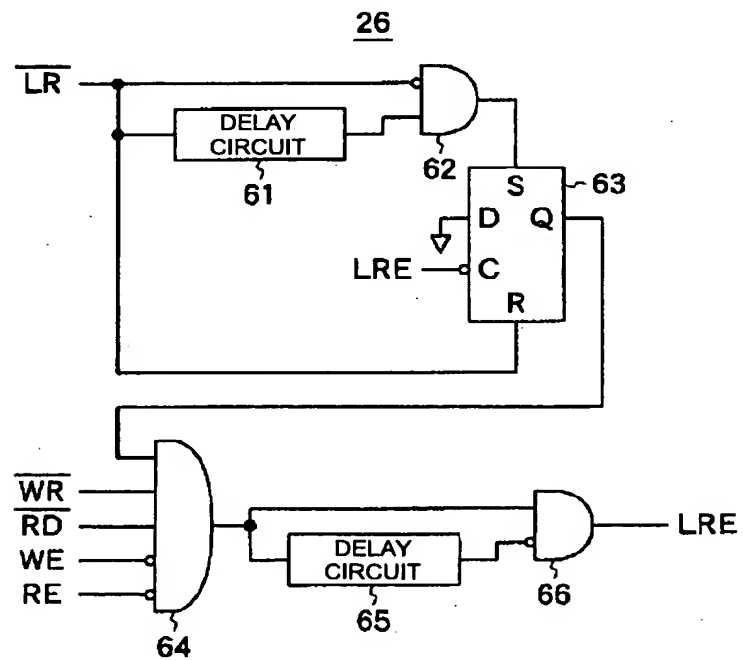


FIG.4



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FIG.5

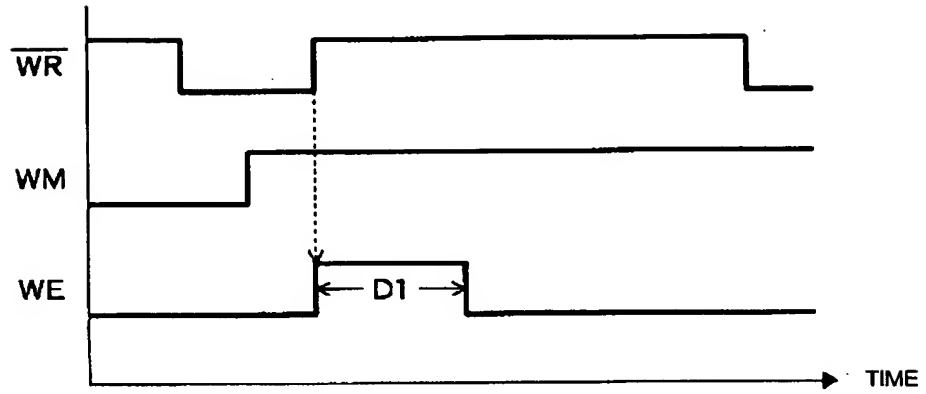
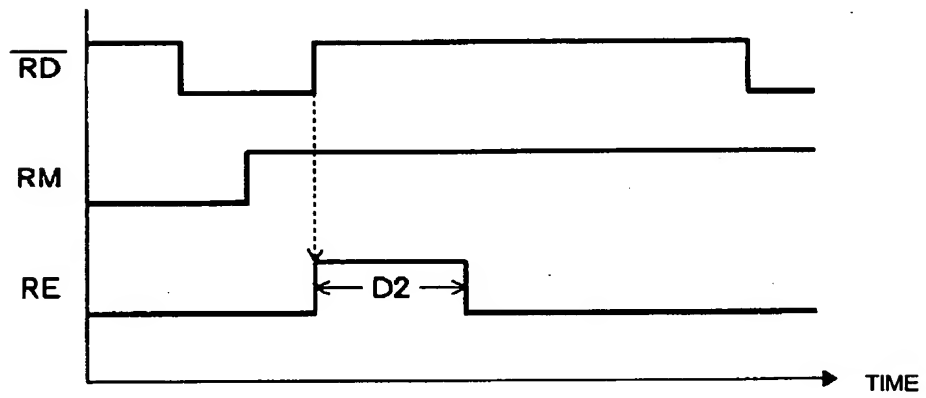
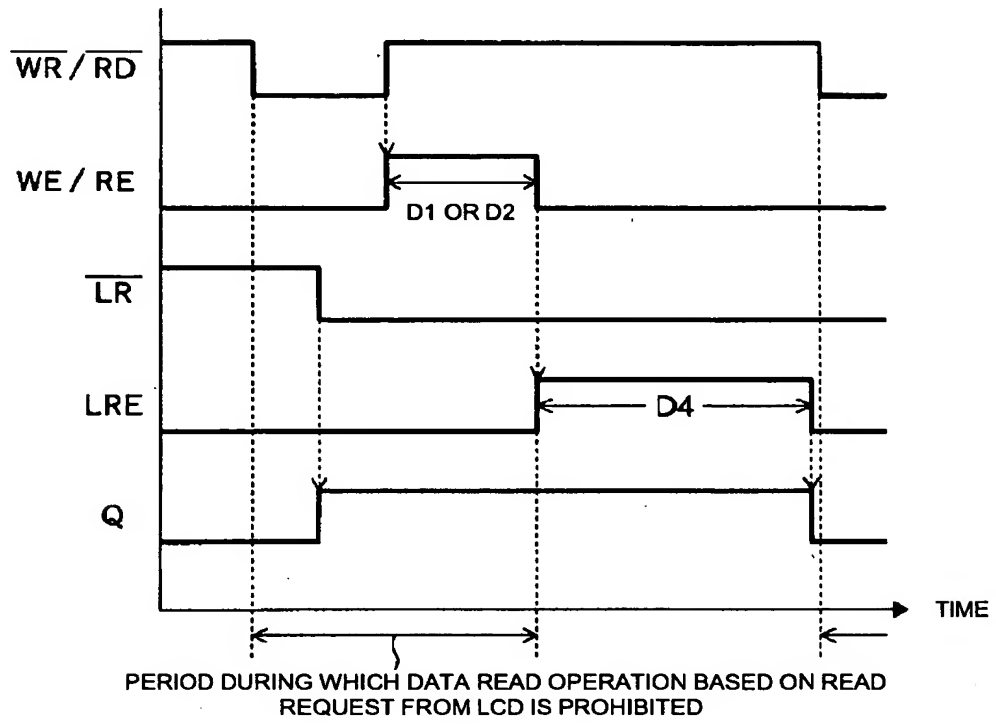


FIG.6



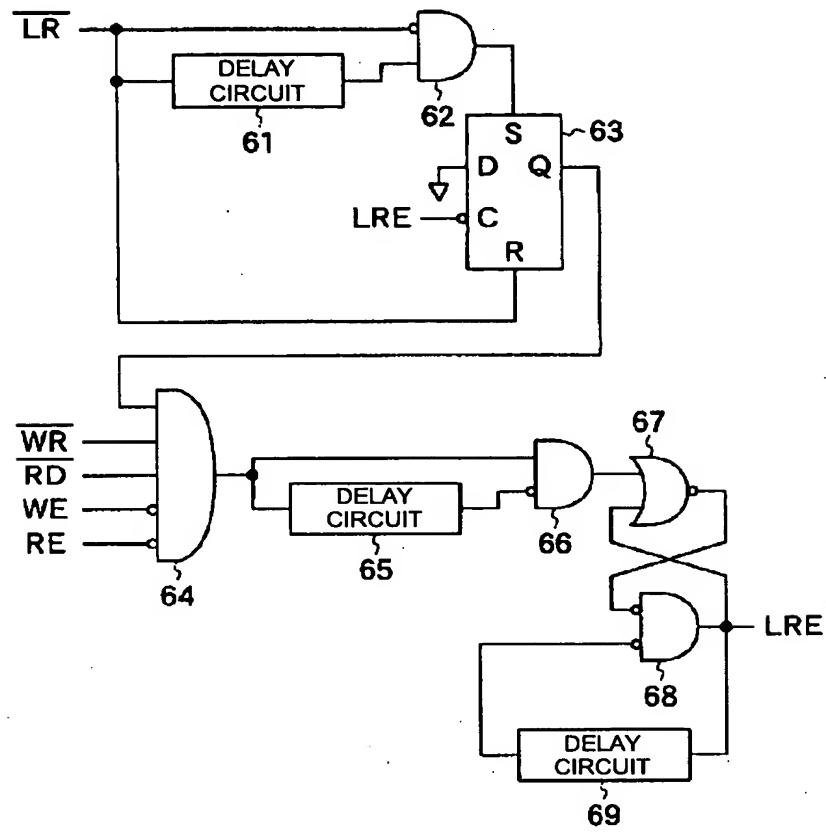
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FIG.7



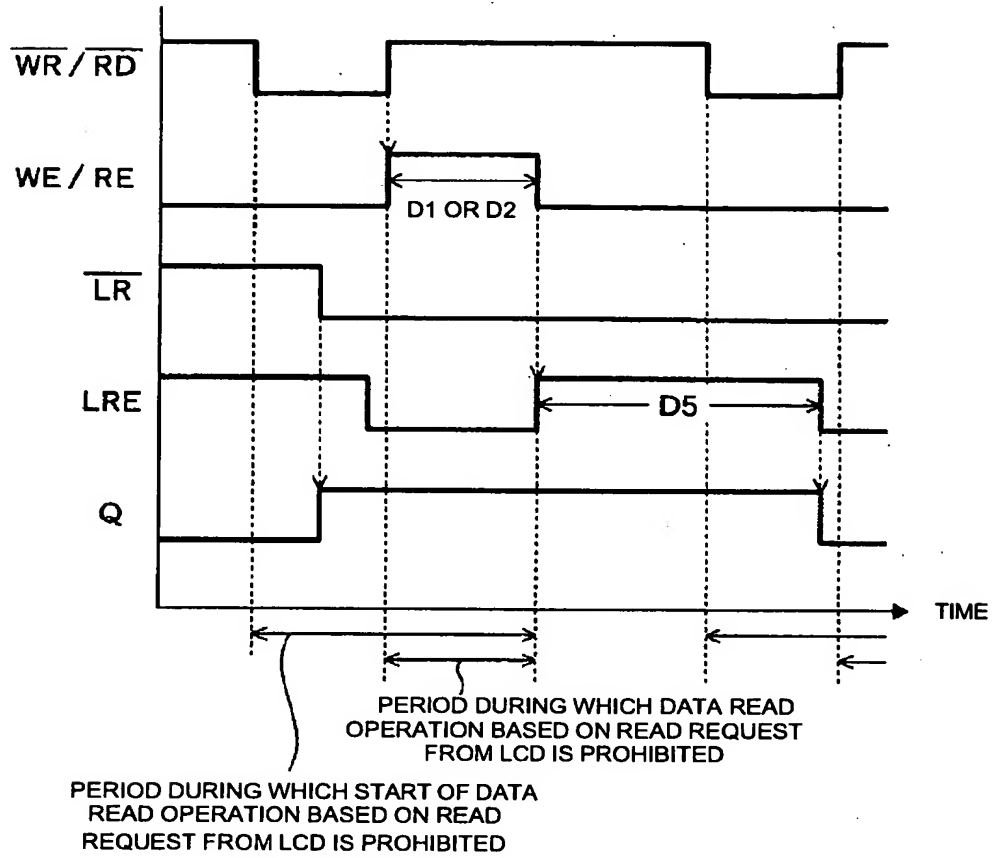
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FIG.8



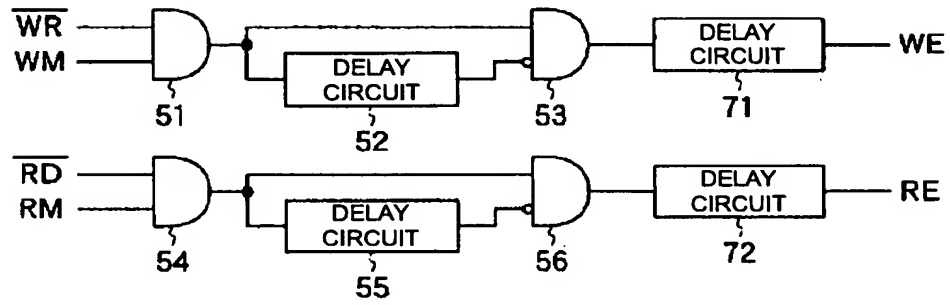
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FIG.9



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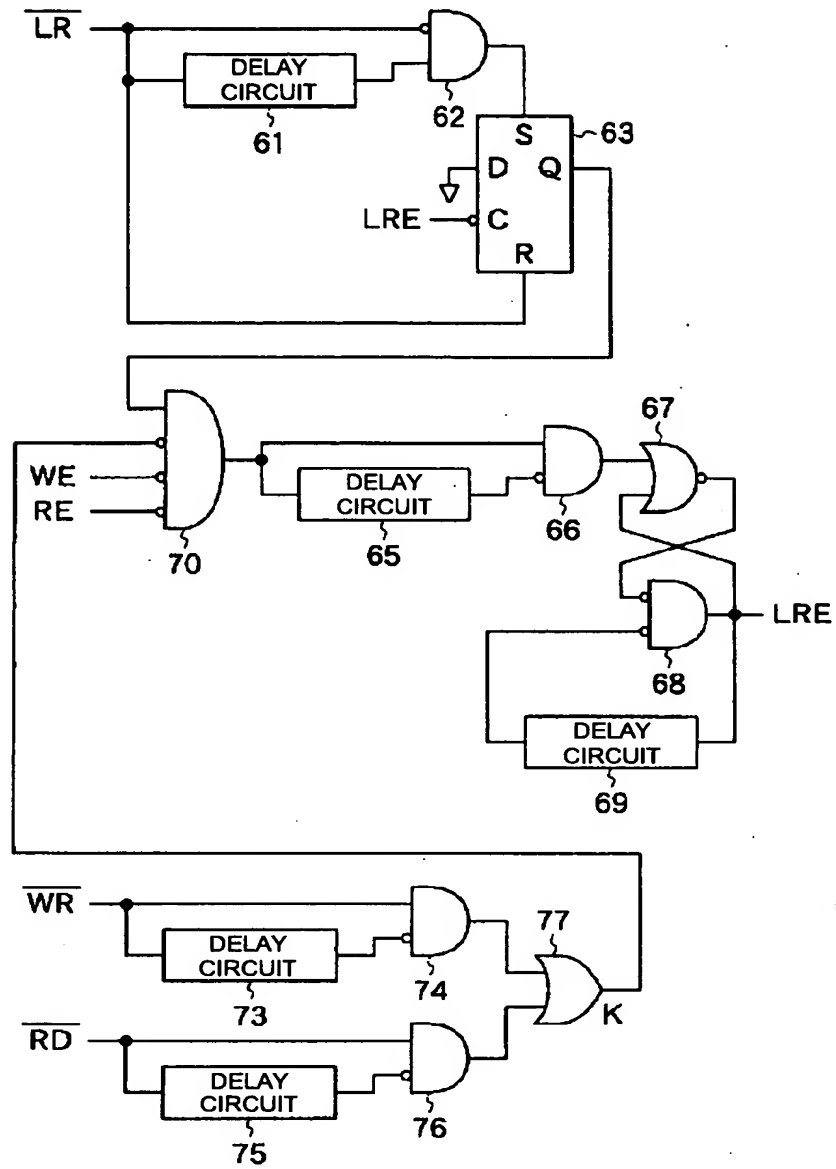
FIG.10





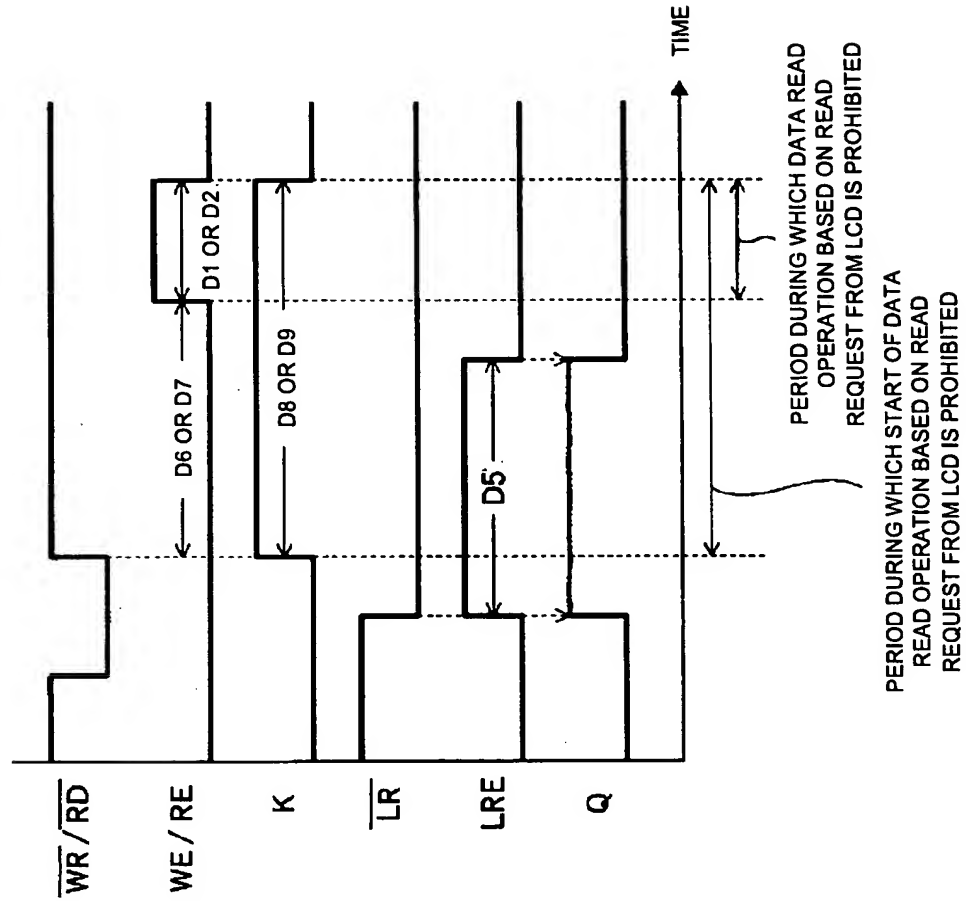
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FIG.11



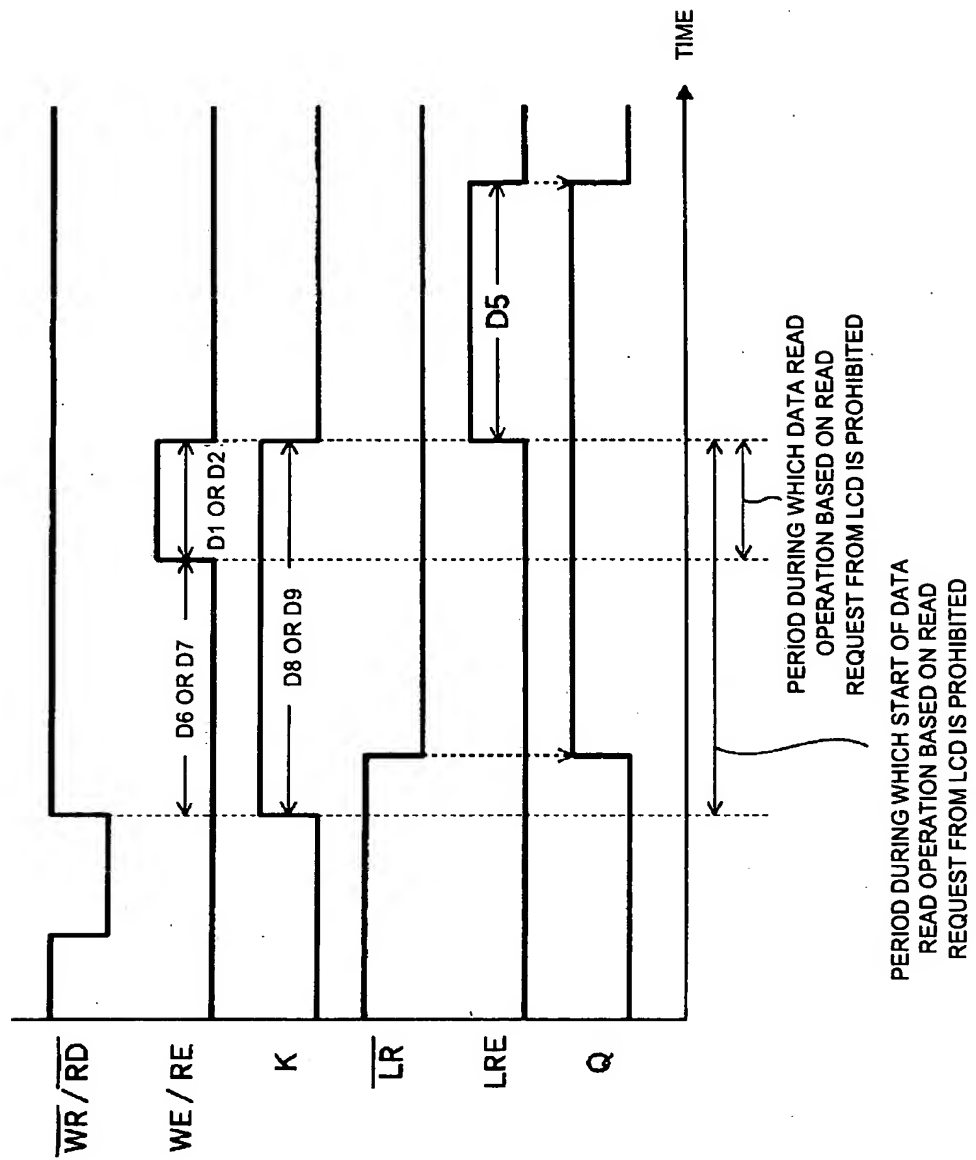
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FIG.12



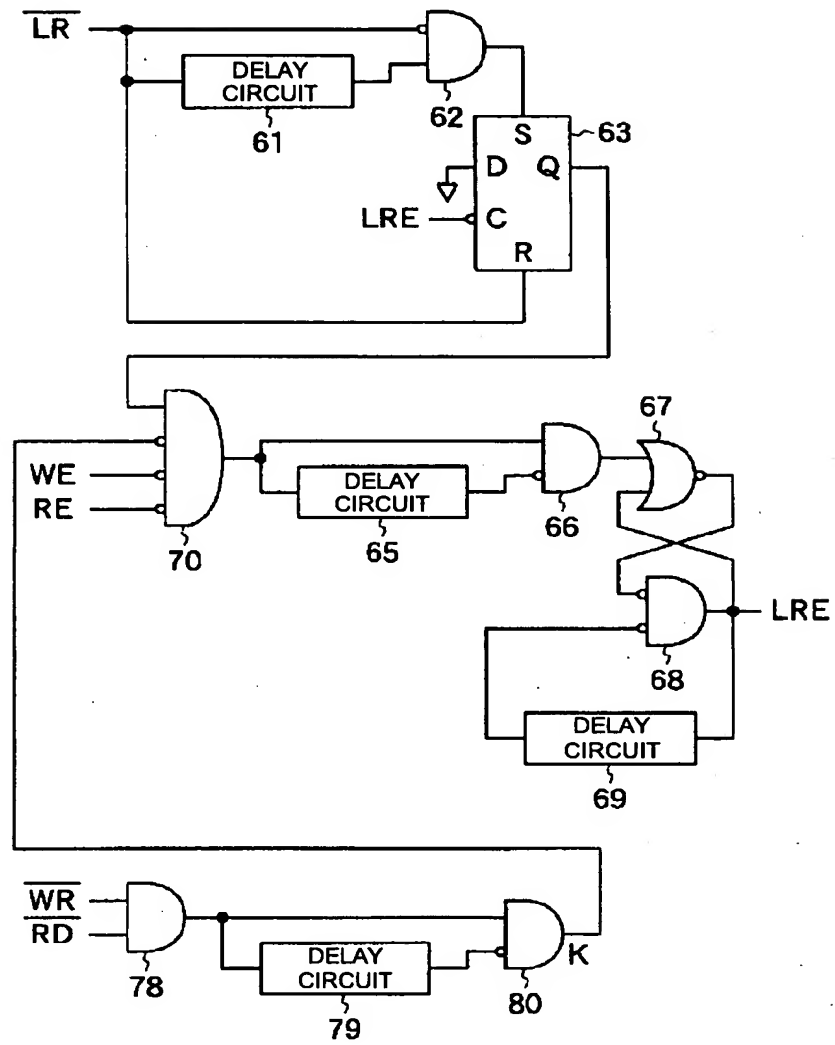
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FIG.13



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FIG.14



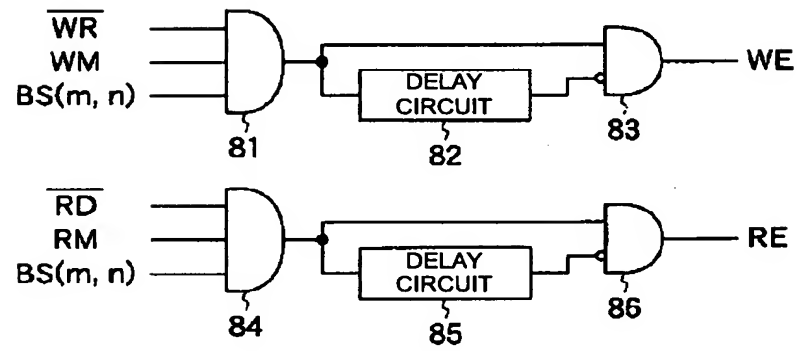
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FIG.15

MEMORY  
CELL ARRAY

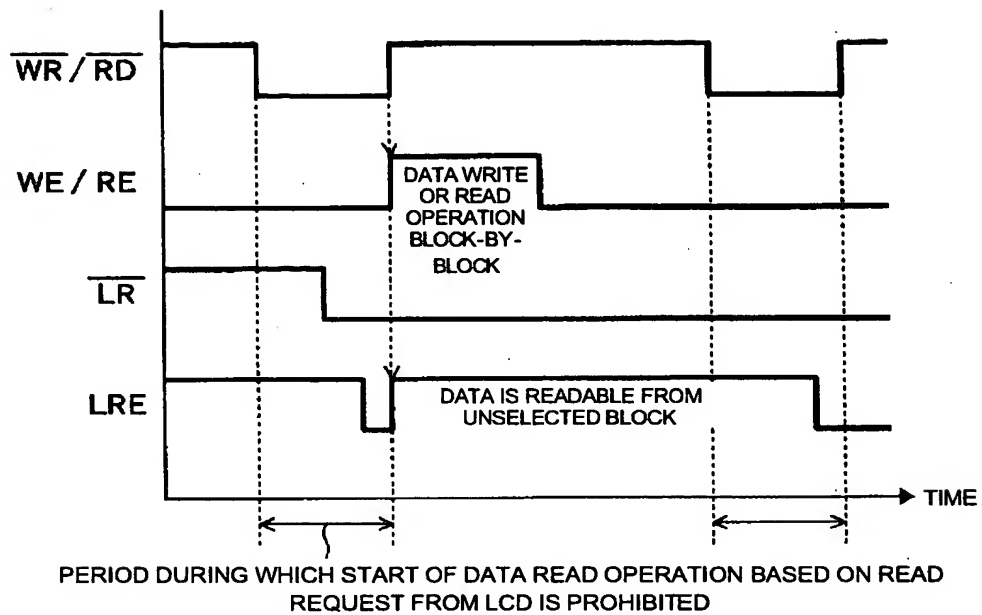
BLOCK (1,1)	BLOCK (1,2)	...	BLOCK (1,N)
BLOCK (2,1)		...	
⋮	⋮	⋮	⋮
BLOCK (M,1)		...	BLOCK (M,N)

FIG.16



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FIG.17



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FIG.18

PRIOR ART

